

ABSTRACT

The invention relates to a method and device to emulate the features of a EEPROM memory device of the type included into a memory macrocell (1) which is embedded into an
5 integrated circuit comprising also a microcontroller and including a Flash EEPROM memory structure formed by a predetermined number of sectors (F0, F1, F2, F3, F4, F5), characterized in that at least two sectors (E0, E1) of the Flash memory structure are used to emulate EEPROM byte
10 alterability.

(Fig. 1)